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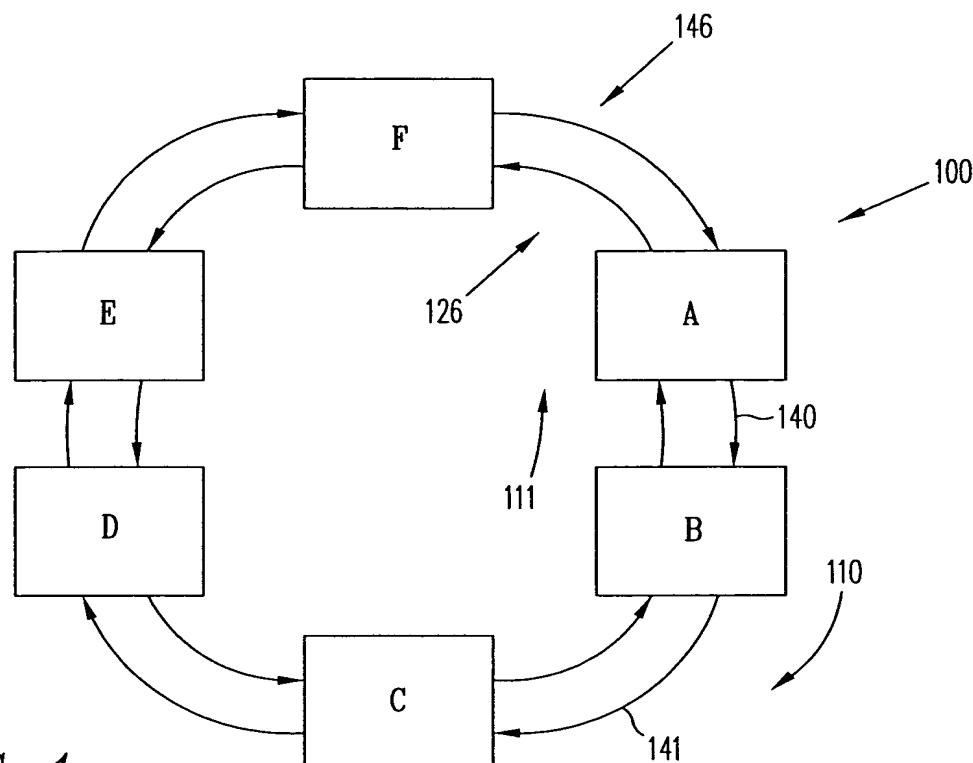


FIG. 1

Prior Art

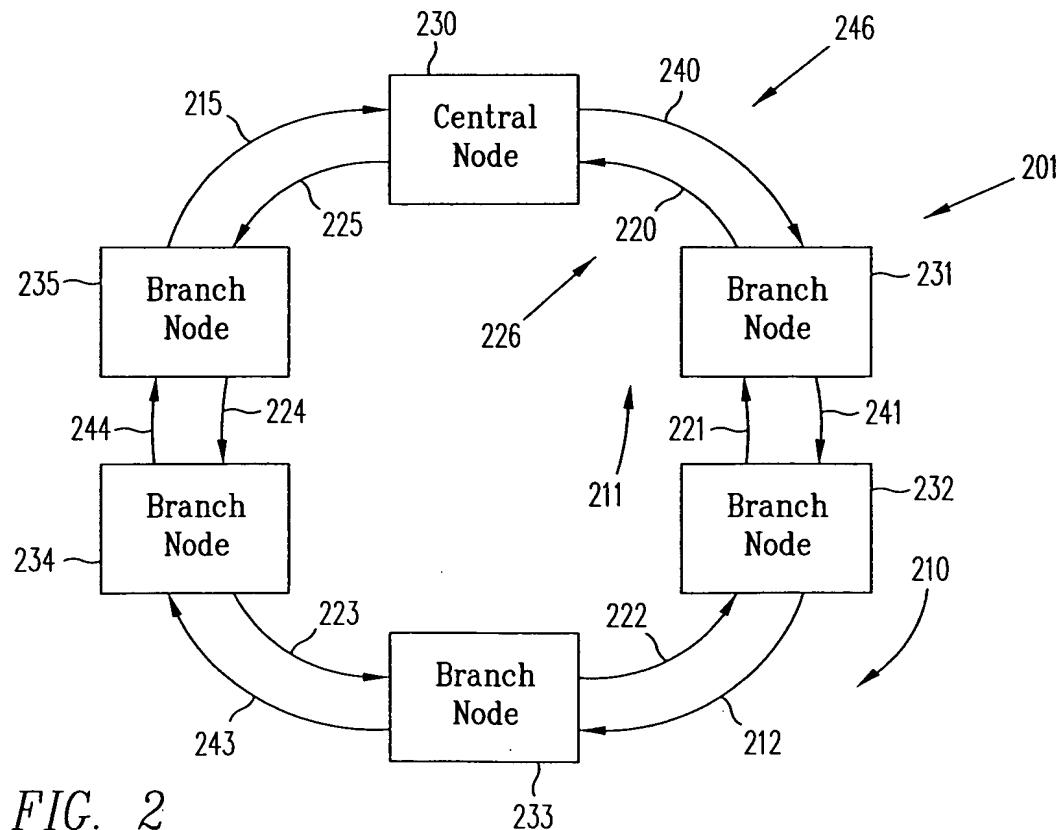


FIG. 2



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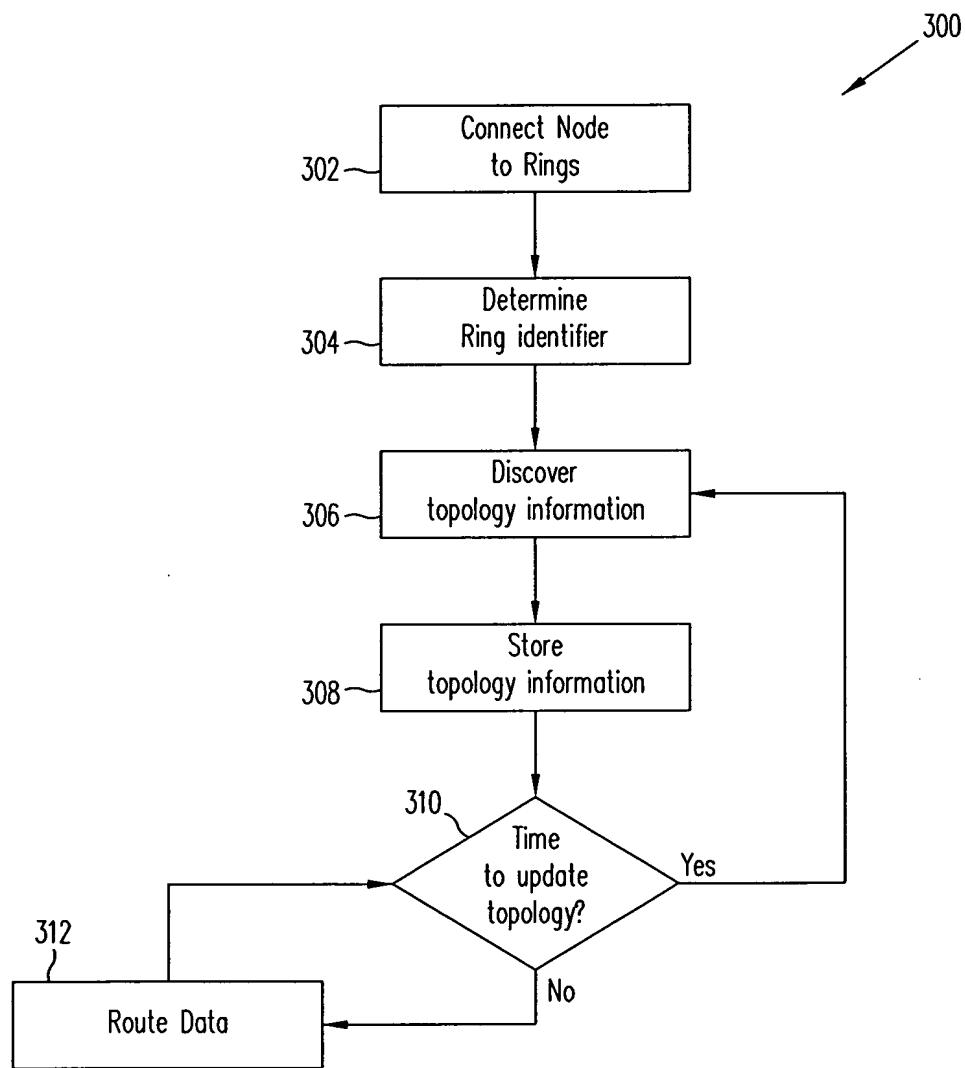


FIG. 3a



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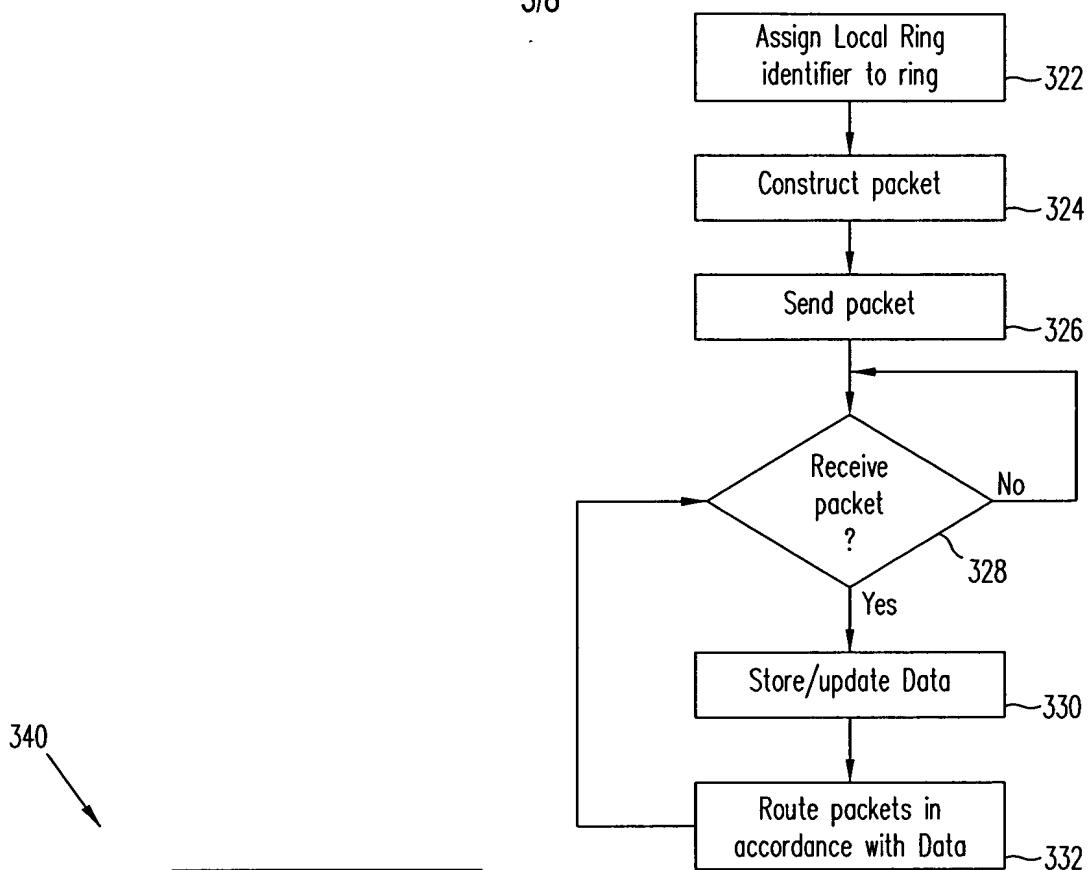


FIG. 3b

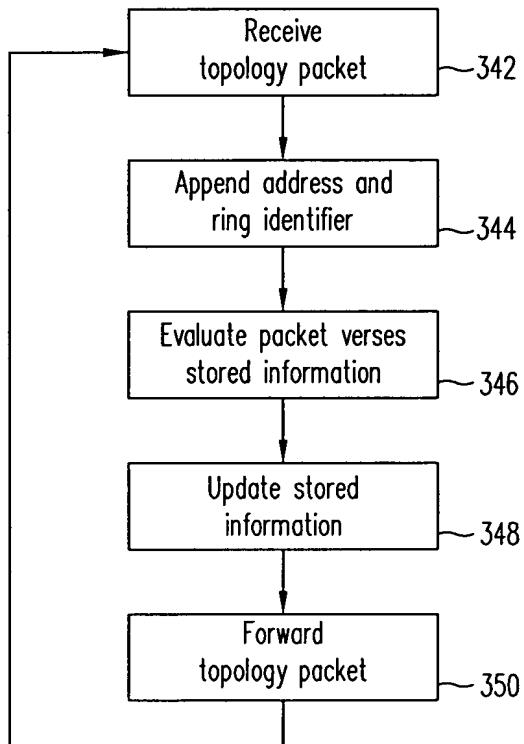


FIG. 3c



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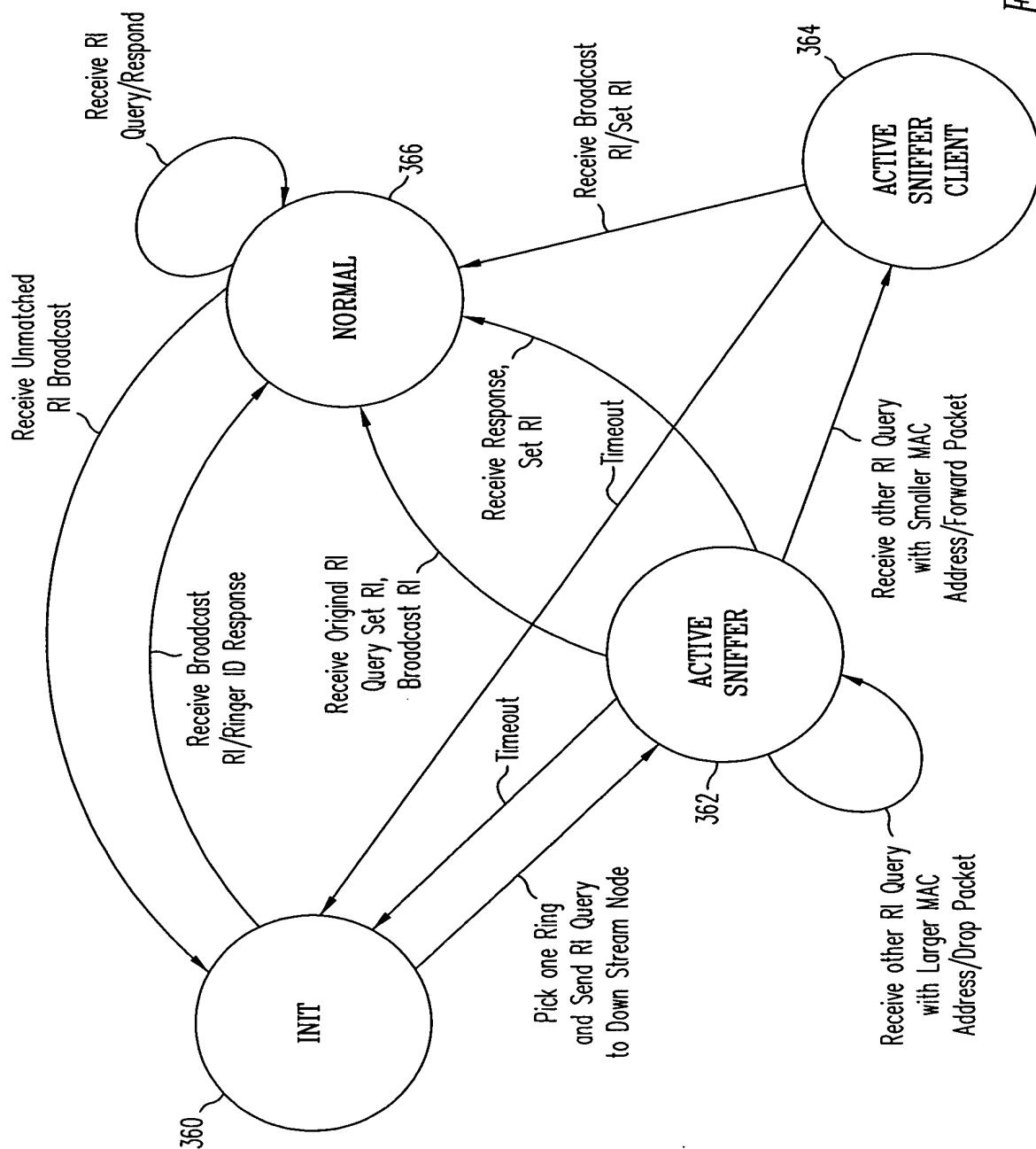


FIG. 3d



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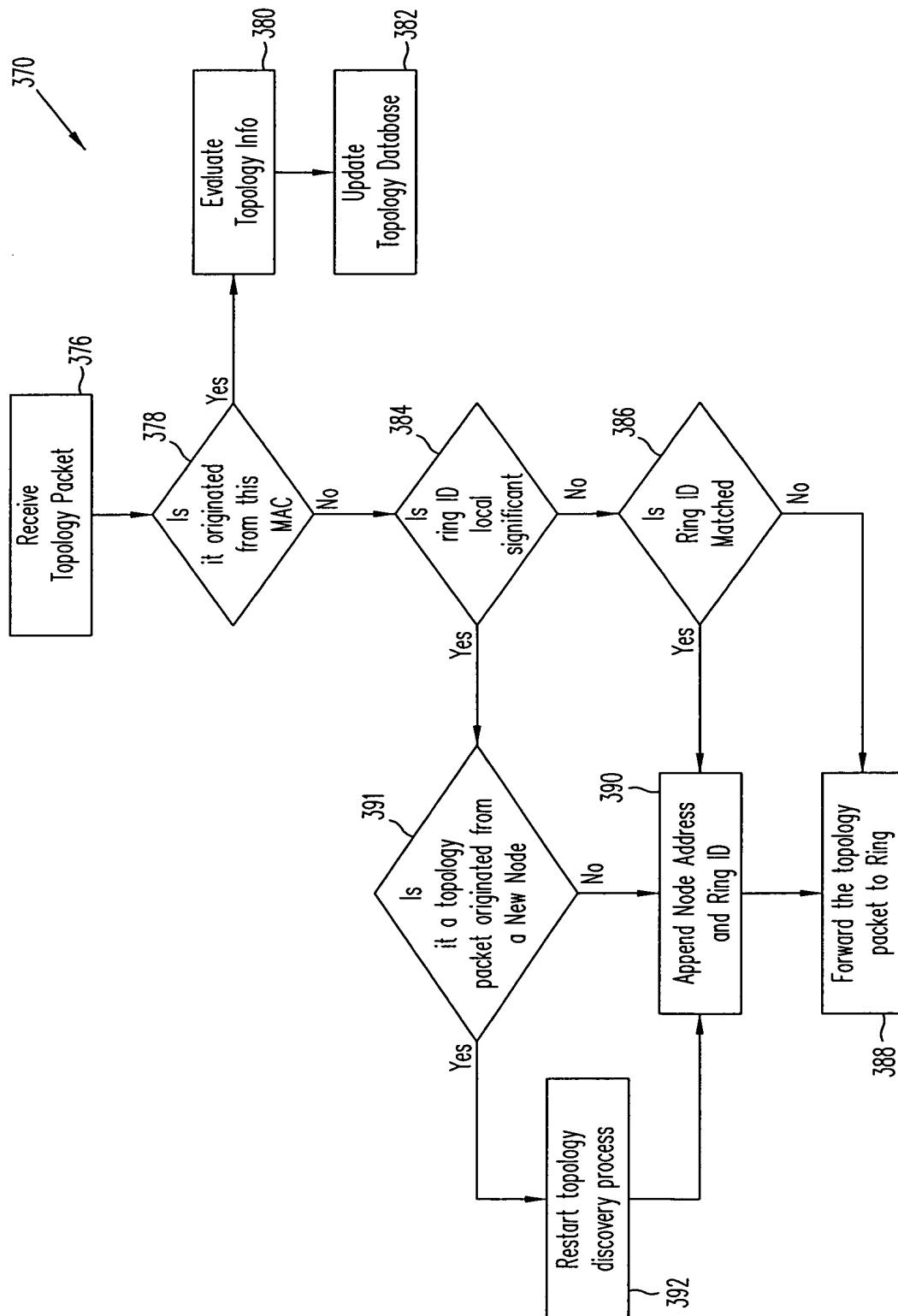


FIG. 3e



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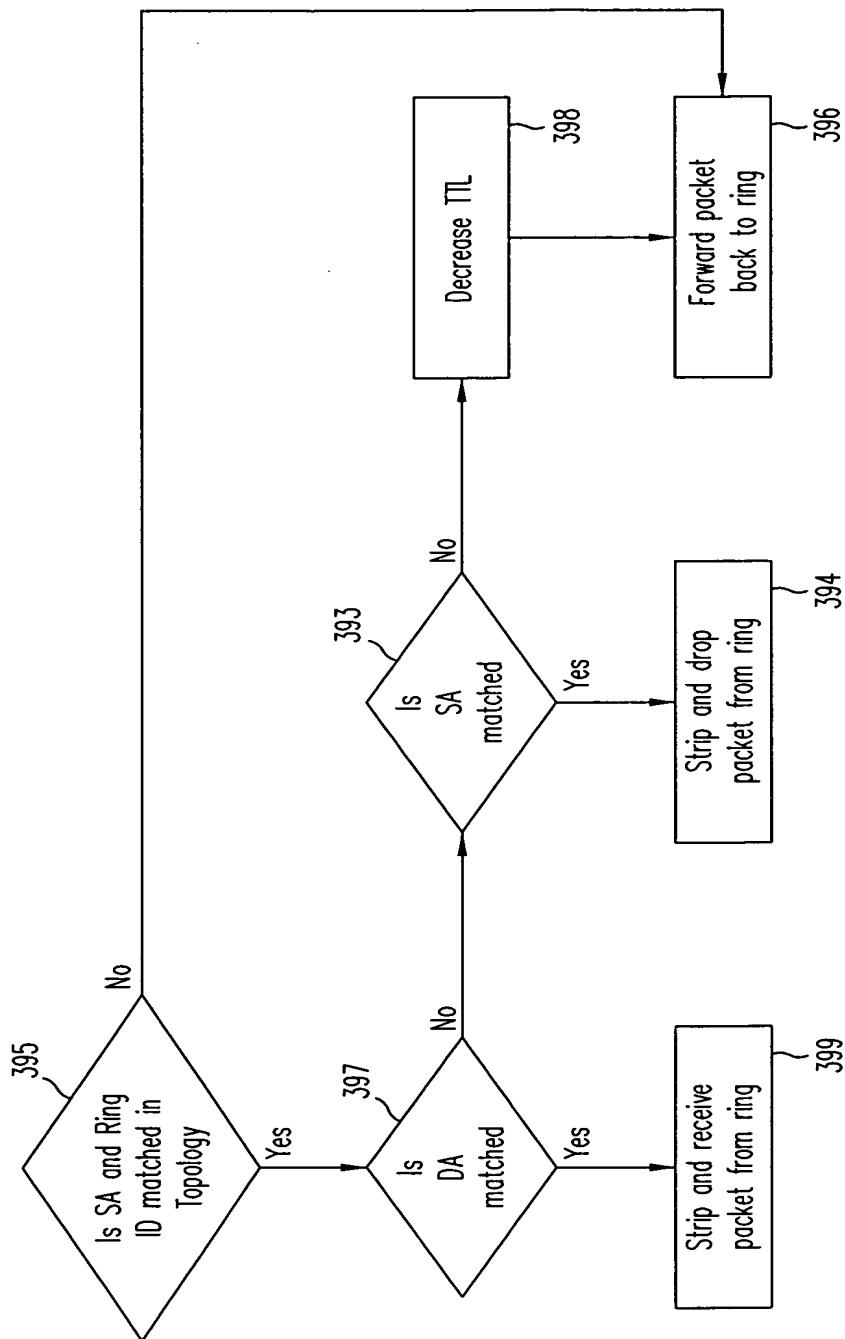
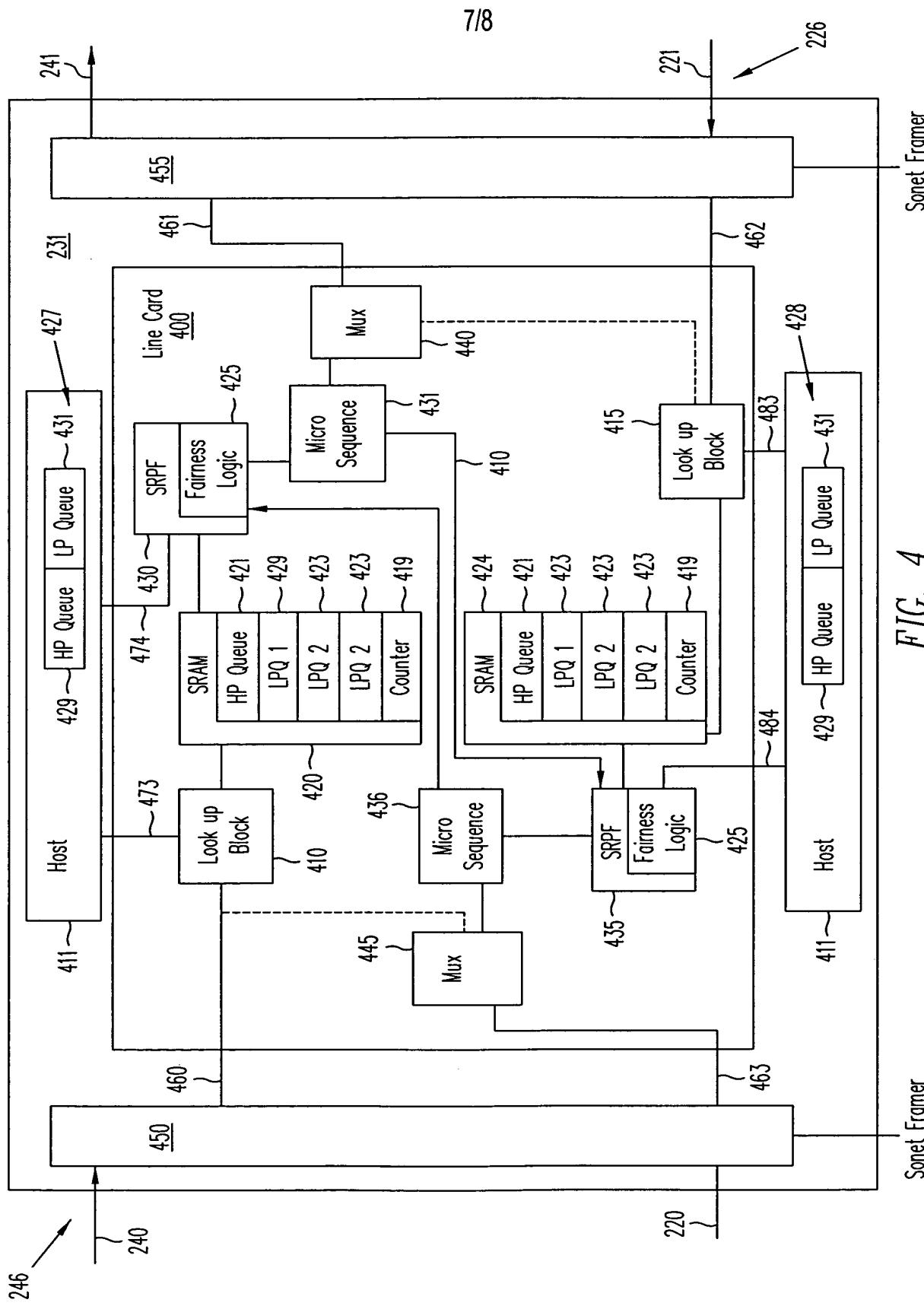


FIG. 3f





Application Serial No.: 09/898,920
 First Named Inventor: Jung-Hong Kao
 Title: Plug And Play Node Addition In A Dual Ring Topology Network

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ADDRESS (MAC, IP)	HOP COUNT INNER	HOP COUNT OUTER	LATENCY INNER	LATENCY OUTER	STATIC RING DIRECTION	DYNAMIC RING DIRECTION	RING ID INNER	RING ID OUTER
12, 23, 46	10	8	1234	1456	1	0	1	0
12, 23, 47	9	9	1200	1400	1	1	1	0

502 ~

500

FIG. 5